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The outcome of the architecture exploration phase is an architectural model at the level of register transfer (RT) accuracy. This model is appropriate to optimize and benchmark the architecture in ...

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Memory Architecture Exploration In modern embedded systems, the area and power consumed by the memory subsystem is up to 10 times that of the data path, making memory a critical component of the design [11]. Further, the memory subsystem constitutes a large part (typically up to 70%) of the silicon area for the current day SoC and it is ...

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On-Chip Memory Architecture Exploration of Embedded System on Chip A Thesis Submitted for the Degree of Doctor of Philosophy in the Faculty of Engineering by T.S. Rajesh Kumar Supercomputer Education and Research Centre Indian Institute of Science Bangalore { 560 012 September 2008.

### **On-Chip Memory Architecture Exploration of Embedded System**

84 ARCHITECTURE EXPLORATION FOR EMBEDDED PROCESSORS Since the HDL model partly contains more structural information on the target architecture than the underlying LISA model, presumptions need to be made on the design of architectural elements, like e.g. the register-file impleÂ-

### **A. Hoffmann et al., Architecture Exploration for Embedded**

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## **ARCHITECTURE EXPLORATION FOR EMBEDDED PROCESSORS WITH LISA PDF**

Architecture Exploration for Energy-Efficient Embedded Vision Applications: From General Purpose Processor to Domain Specific Accelerator Maria Malik 1, Farnoud Farahmand , Paul Otto , Nima Akhlaghi , Tinoosh Mohsenin3, Siddhartha Sikdar2, Houman Homayoun1 1Department of Electrical and Computer Engineering, 2Department of Bioengineering, George Mason University, Fairfax County, {mmalik9 ...

### **Architecture Exploration for Energy-Efficient Embedded**

Today's SoCs are complex designs with multiple embedded processors, memory subsystems, and application specific peripherals. The memory architecture of embedded SoCs strongly influences the power and performance of the entire system.

### **On-chip memory architecture exploration framework for DSP**

The memory architecture of the embedded system strongly influences these parameters. Hence the embedded system designer performs a complete memory architecture exploration. This problem is a multi-objective optimization problem and can be tackled as a two-level optimization problem.

### **MAX: A Multi Objective Memory Architecture eXploration**

Architecture exploration for embedded systems is becoming an indispensable tool for System-on-Chip designers. This process requires the evaluation of many architectures that are generated during ...

### **(PDF) A methodology for modeling embedded processors for**

Memory Architecture Exploration for Programmable Embedded Systems addresses efficient exploration of alternative memory architectures, assisted by a "compiler-in-the-loop" that allows effective matching of the target application to the processor-memory architecture. This new approach for memory ...

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