

ion implantation in semiconductors pdf

"Ion Implantation," (Proceedings of the First International Ion Implantation Conference) , Gordon and Breach, New York 1971. I. Ruge and J. Graul , eds. , "Ion Implantation in Semiconductors," (Proceedings of the Second International Ion Implantation Conference) , Springer-Verlag, Berlin 1971.

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Ion implantation was first applied to semiconductors over 30 years ago as a means of introducing controllable concentrations of n- and p-type dopants at precise depths below the surface.

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of ion incidence angle ($<0.5^\circ$ for critical implants) place tight constraints on ion beam stability, scan uniformity, and beam collimation in the area near the wafer surface. Beamline System Types Each of the wide variety of ion implantation beamline designs contains certain general groups of functional components (see Fig. 4).

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makes ion implantation practical. Ion energy requirements vary from less than 1 keV to more than 3,000 keV. Accelerating ions to higher energies is widely today in semiconductor manufacturing. A modern ion implanter costs about \$2-5 million, depending on the model and the wafer size it processes. Of

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