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Logic Synthesis and Verification Algorithms

Logic Synthesis and Logic Synthesis and Verification Jie-Hong Roland Jiang æ±Ÿä»á• Department of Electrical Engineering National Taiwan University

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Synthesis and Verification of Digital Systems

Research and development of logic synthesis and verification have matured considerably over the past two decades. Many commercial products are available, and they have been critical in harnessing advances in fabrication technology to produce today's plethora of electronic components. While this

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