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about circuits, in fact they are the foundation of a thriving industry which places a premium on correctness. However, the contrast between hardware and software in terms of reasoning techniques is striking. Our primary motivation is to address a surprising method-ological and conceptual gap, the paucity of syntactic models for digital circuits.

Categorical Semantics of Digital Circuits

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consider digital circuits as broadly construed, i.e. processing a finite number of discrete input and output levels, with explicit and discrete delays down to a smallest observable delay (ϵ). This level of abstraction applies to Boolean circuits but also to transistor-level modelling, if the transistor operates in non-linear mode.

Categorical Semantics of Digital Circuits - cs.bham.ac.uk

for WYN WILLIAMS Department of Computer Science Manchester University Manchester, United Kingdom Semantics of Digital Circuits. By Carlos Delgado Kloos. Lecture Notes in Computer Science 285, Springer, Berlin, 1987, 119 pages, Price DM 27.00, ISBN 3 540 18540 2.

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Semantics Of Digital Circuits (Lecture Notes In Computer

Diagrammatic Semantics for Digital Circuits Dan R. Ghica¹, Achim Jung¹, and Aliaume Lopez² ¹ University of Birmingham ² ENS Cachan, Université Paris-Saclay Abstract We introduce a general diagrammatic theory of digital circuits, based on connections between

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Quite complex digital logic circuits (e.g. entire computers) can be built using a few types of basic circuits called gates, each performing a single elementary logic operation : NOT, AND, OR, NAND , NOR , etc..

DIGITAL LOGIC CIRCUITS - University of Ottawa

Abstract: We introduce a general diagrammatic theory of digital circuits, based on connections between monoidal categories and graph rewriting. The main achievement of the paper is conceptual, filling a foundational gap in reasoning syntactically and symbolically about a large class of digital circuits (discrete values, discrete delays, feedback).

[1703.10247] Diagrammatic Semantics for Digital Circuits

design combinational logic circuits $\hat{=}$ Combinational logic circuits do not have an internal stored state, i.e., they have no memory. Consequently the output is solely a function of the current inputs. $\hat{=}$ Later, we will study circuits having a stored internal state, i.e., sequential logic circuits.

Digital Electronics Part I $\hat{=}$ Combinational and Sequential

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Semantics of Digital Circuits | Carlos Delgado Kloos

been given for asynchronous circuits at the PRS level. Because of the intrinsically concurrent semantics of rewriting logic, a rewriting semantics is a perfect candidate for this task. Therefore, in this paper we provide the first ever formal executable semantics of asynchronous circuits at the PRS level using rewriting logic and Maude [11].

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