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Ranjit Gharpurey and Edoardo Charbon, "Substrate Coupling: Modeling, Simulation and Design Perspectives," in Proceedings of the International Symposium on Quality Electronic Design, pp. 283-290, 2004. * Afzali-Kusha et al., "Substrate Noise Coupling in SoC Design: Modeling, Avoidance, and Validation," in Proceedings of the IEEE, 2006.

Substrate Coupling: Modeling and Mitigation Techniques

Index Terms—Substrate coupling noise, substrate extraction, modeling techniques, noise mitigation techniques, isolation techniques. I. INTRODUCTION THE increased demand for high performance and low power has resulted into integration of RF, digital, and analog circuits together on same substrate. ...

Substrate Coupling Noise: Modeling and Mitigation Techniques

preventing substrate noise coupling issues in large systems efficiently. We are primarily interested in modeling the epitaxial-type heavily-doped bulk substrate, that is commonly

Substrate Coupling Analysis and Noise Reduction Methods

Substrate noise coupling in integrated circuits (ICs) is the process by which interference signals in the form of voltage and current glitches cause parasitic currents to flow in the silicon substrate to various parts of the IC.

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Substrate Coupling and Interconnect Noise in Mixed-Signal and High Speed Digital ICs Eby G. Friedman
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Substrate Coupling and Interconnect Noise in Mixed

Substrate noise coupling has become and continues to be one of the bottleneck challenges in mixed-signal integrated circuit design and system-on-chip (SOC) integration, where the sensitive analog or RF circuits suffer from the harmful effects of noise

SYNTHESIZED COMPACT MODELS FOR SUBSTRATE NOISE COUPLING IN

signals and to identify critical substrate coupling paths. Substrate noise sensing techniques have been presented so far in [3–7]. None, however, provides capturing of substrate noise from the low MHz frequency region to the wireless communications GHz region and only

CMOS 65 nm 'on chip' broadband real time substrate noise

Similarly, in the case of crosstalk between analog nodes, a signal can couple from one node to another via the substrate. This phenomenon is referred to as substrate coupling or substrate noise coupling. Modelling, analysis, and verification of mixed signal coupling. There is a sizeable literature on substrate, and mixed signal coupling.

Substrate coupling - Wikipedia

Accurate simulation of substrate coupling has only recently begun to receive attention [4]–[6]. A device simu-

lation program has been used to study the mechanism of substrate coupling noise [4], [5]; however, its applicability for simulating integrated circuits is limited by the long simulation times required.

Addressing substrate coupling in mixed-mode ICs

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